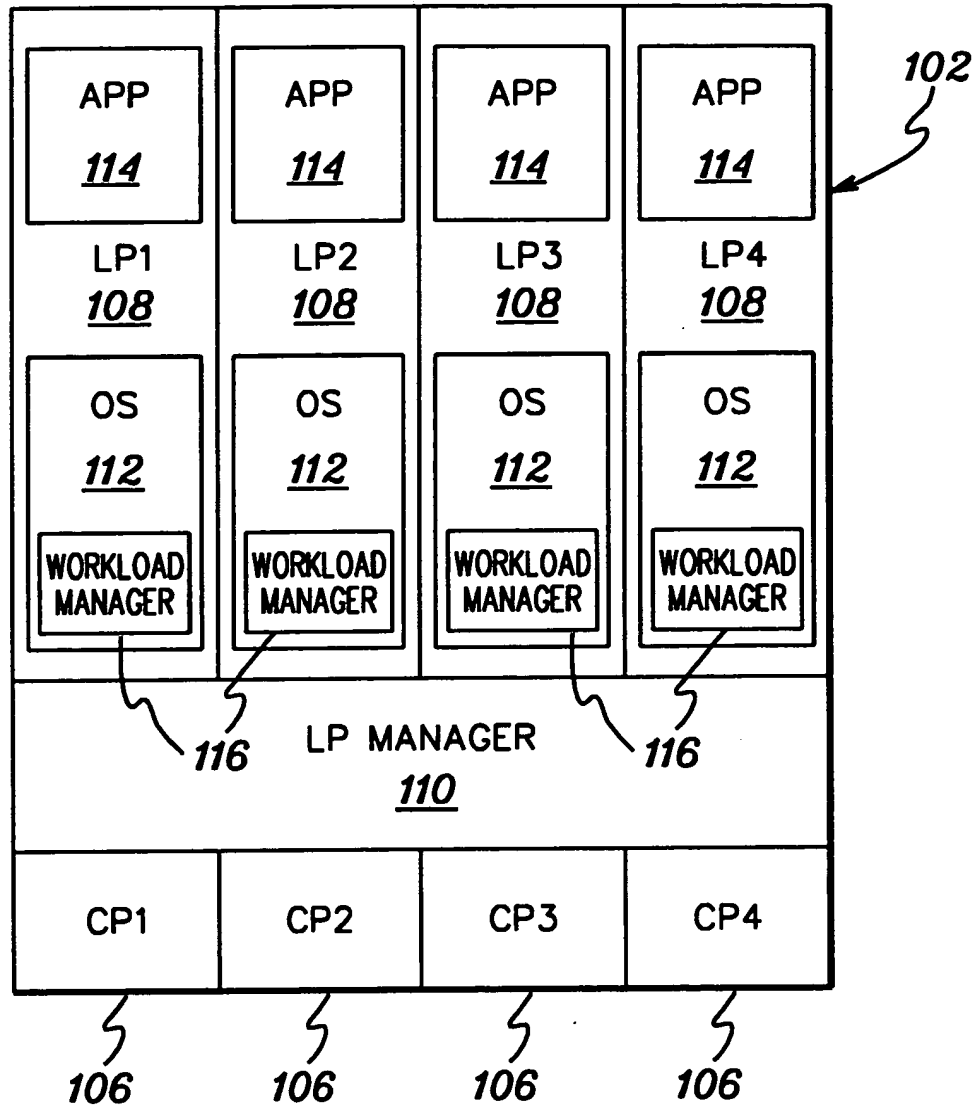


100



*fig. 1A*

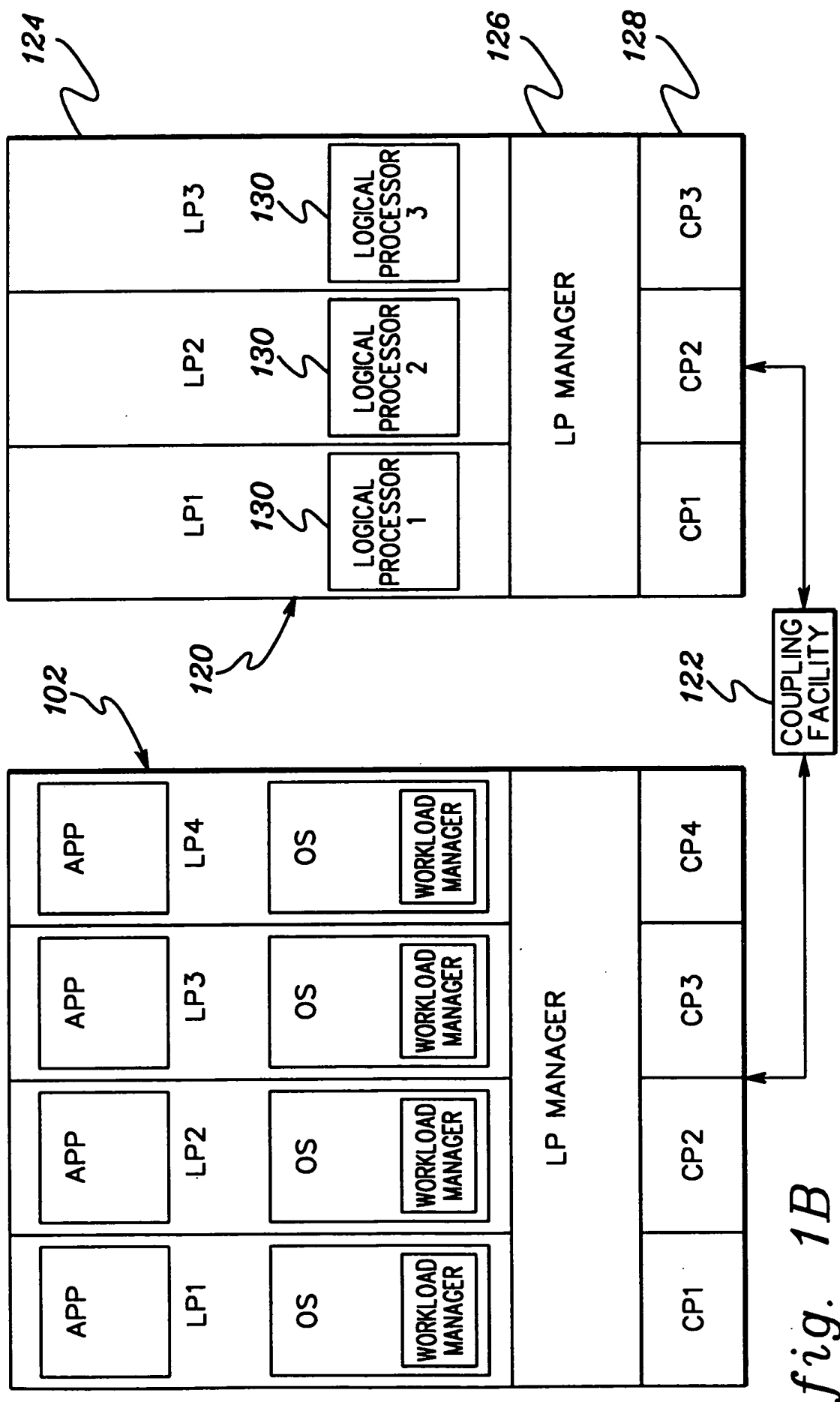
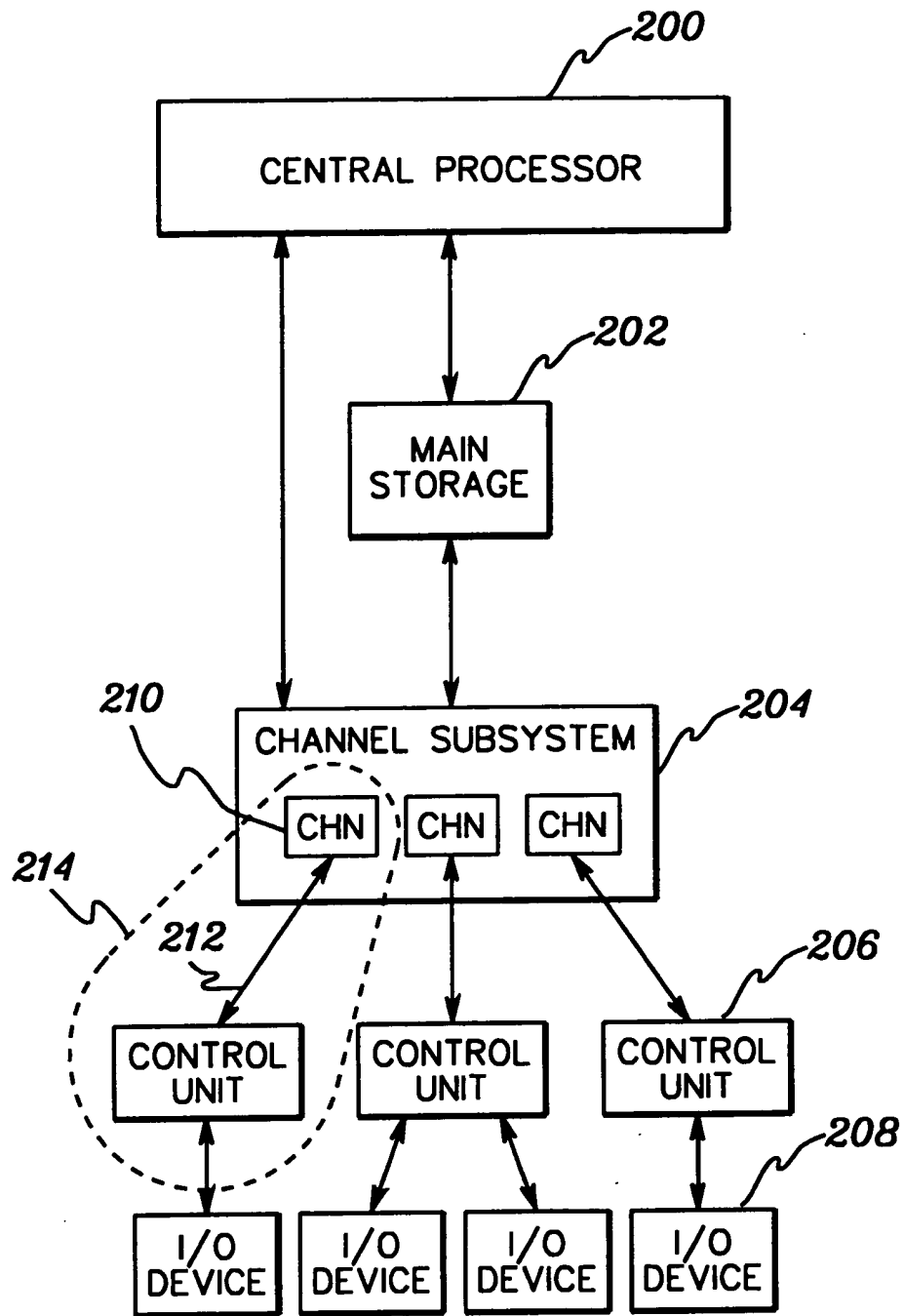
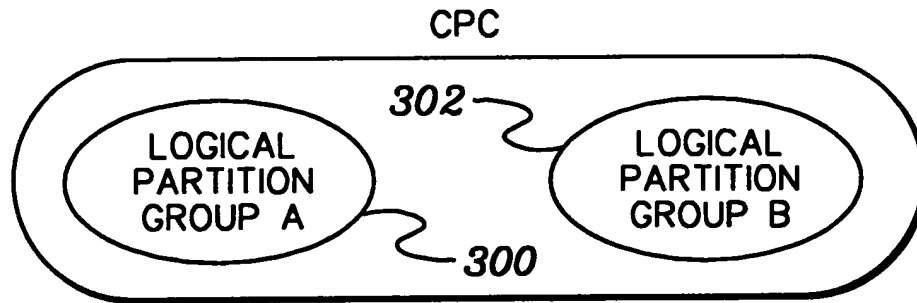


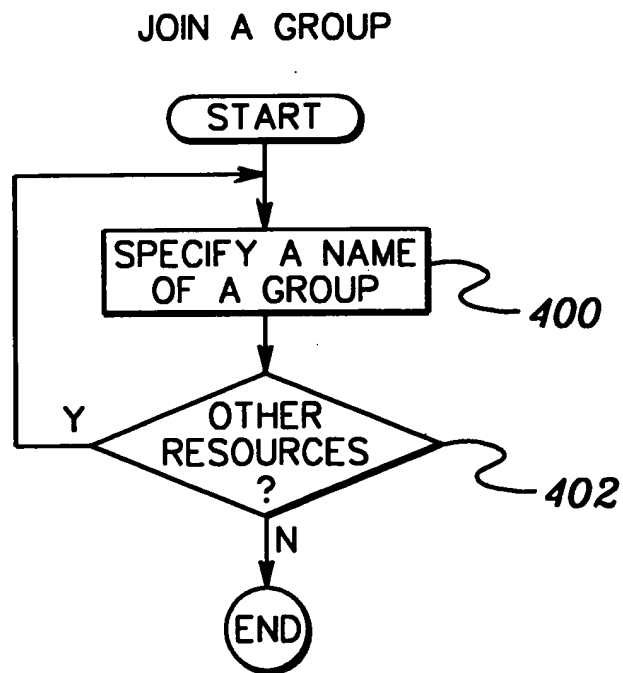
fig. 1B



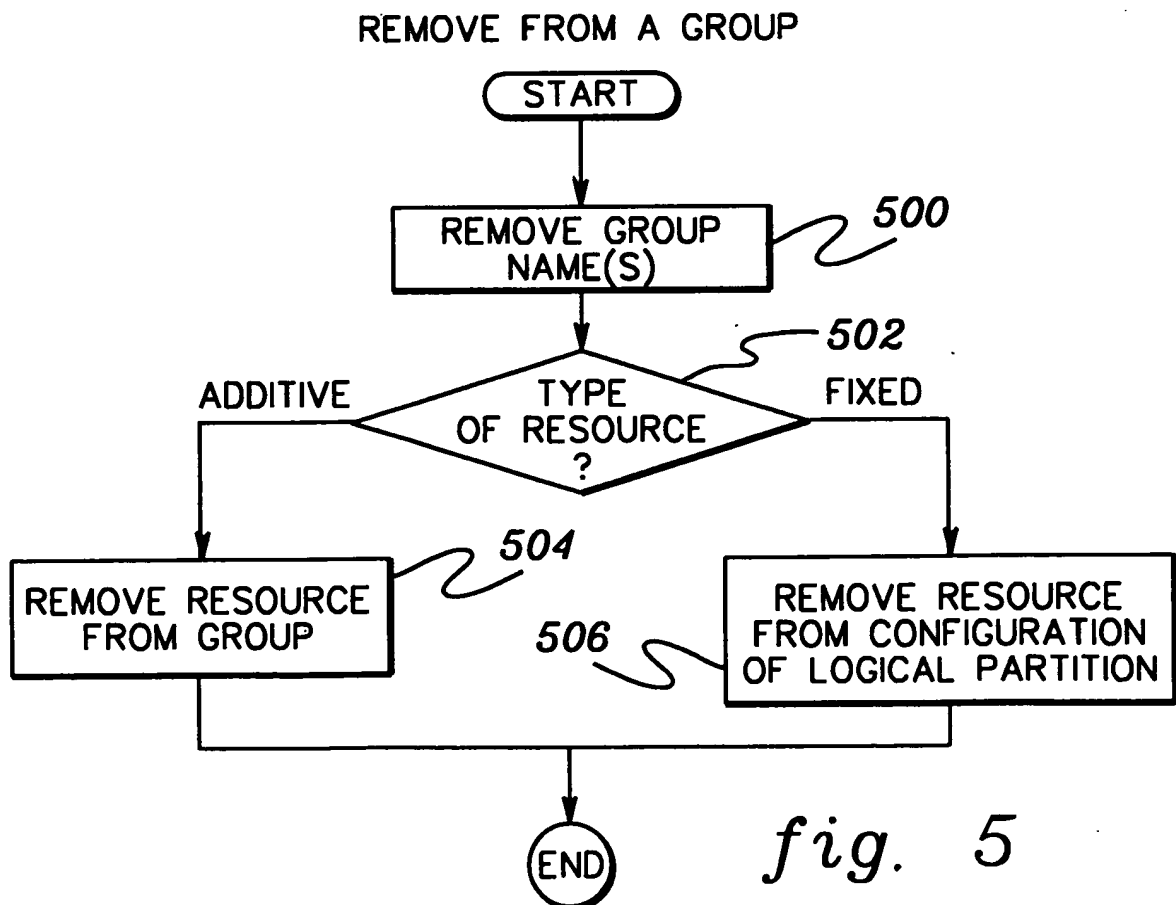
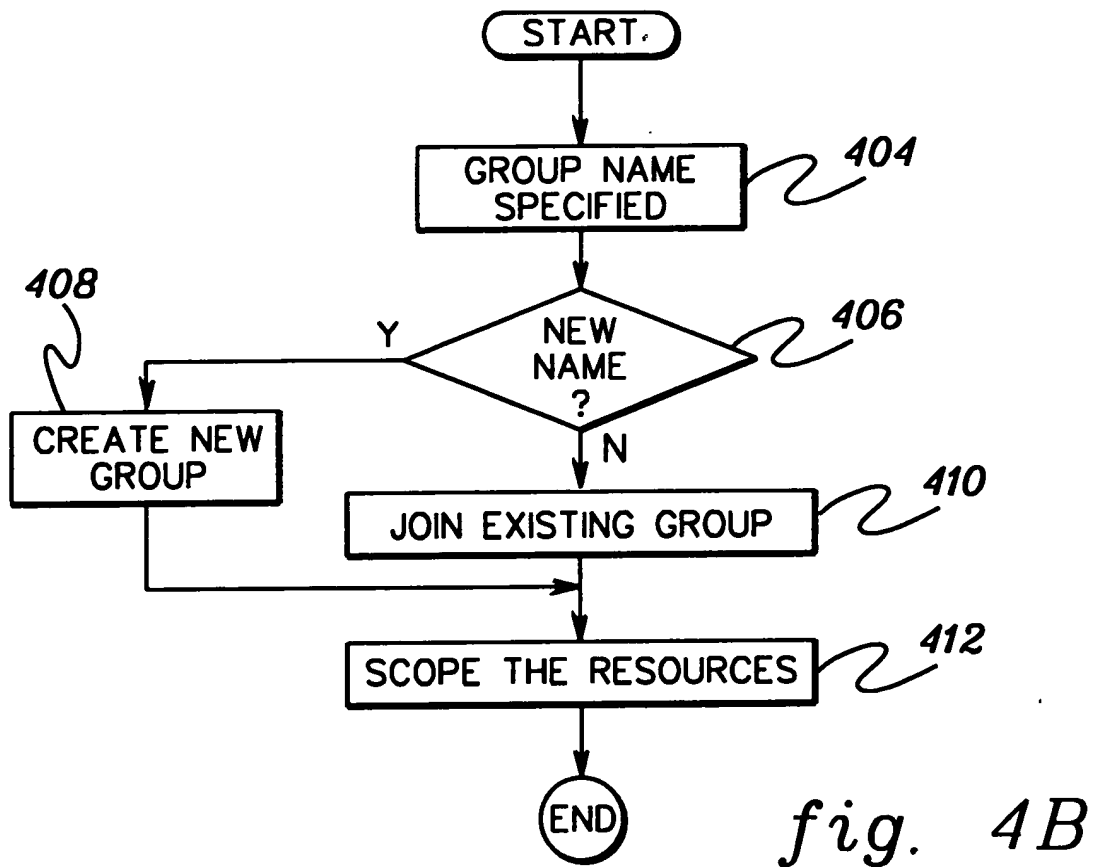
*fig. 2*



*fig. 3*



*fig. 4A*



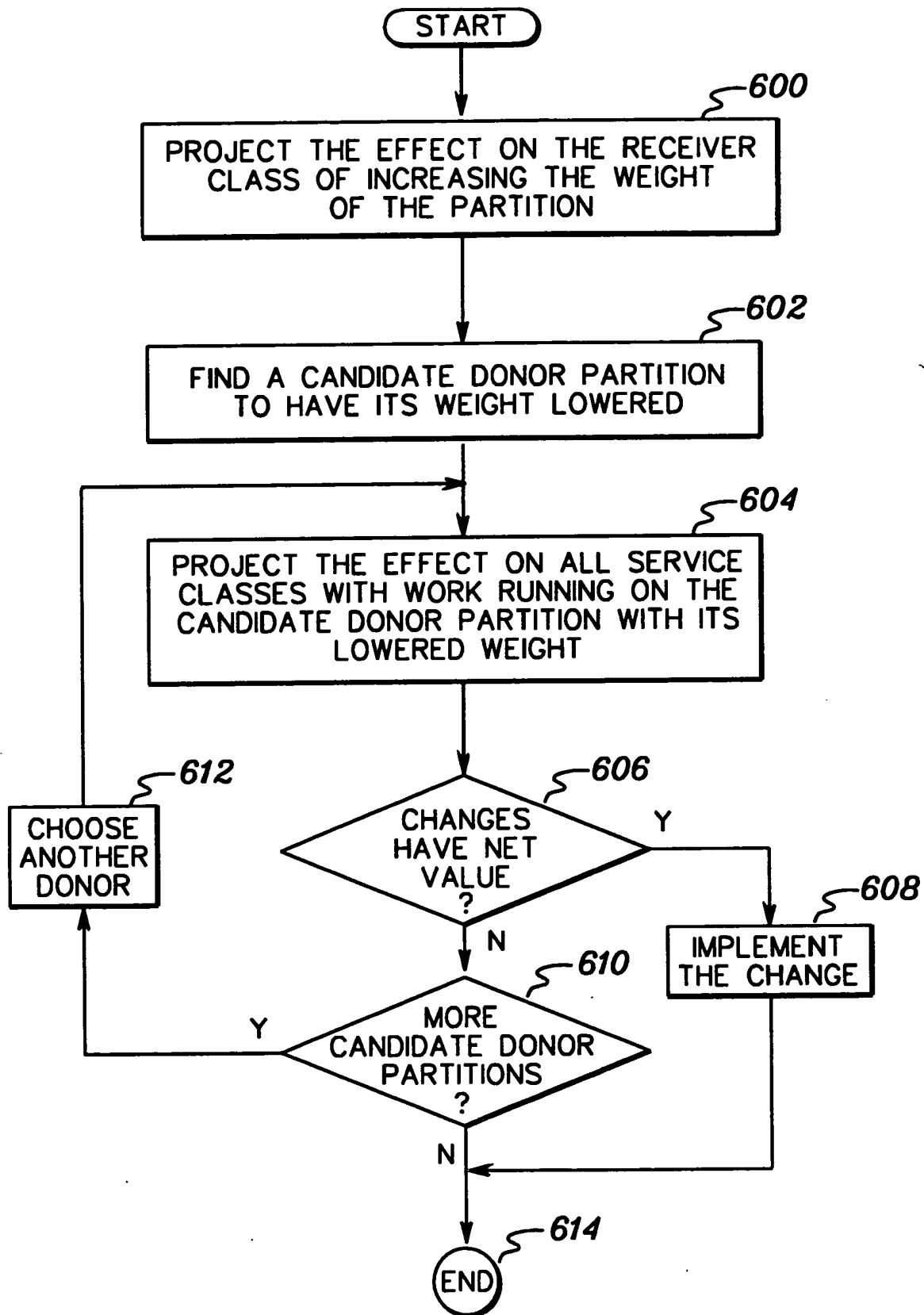
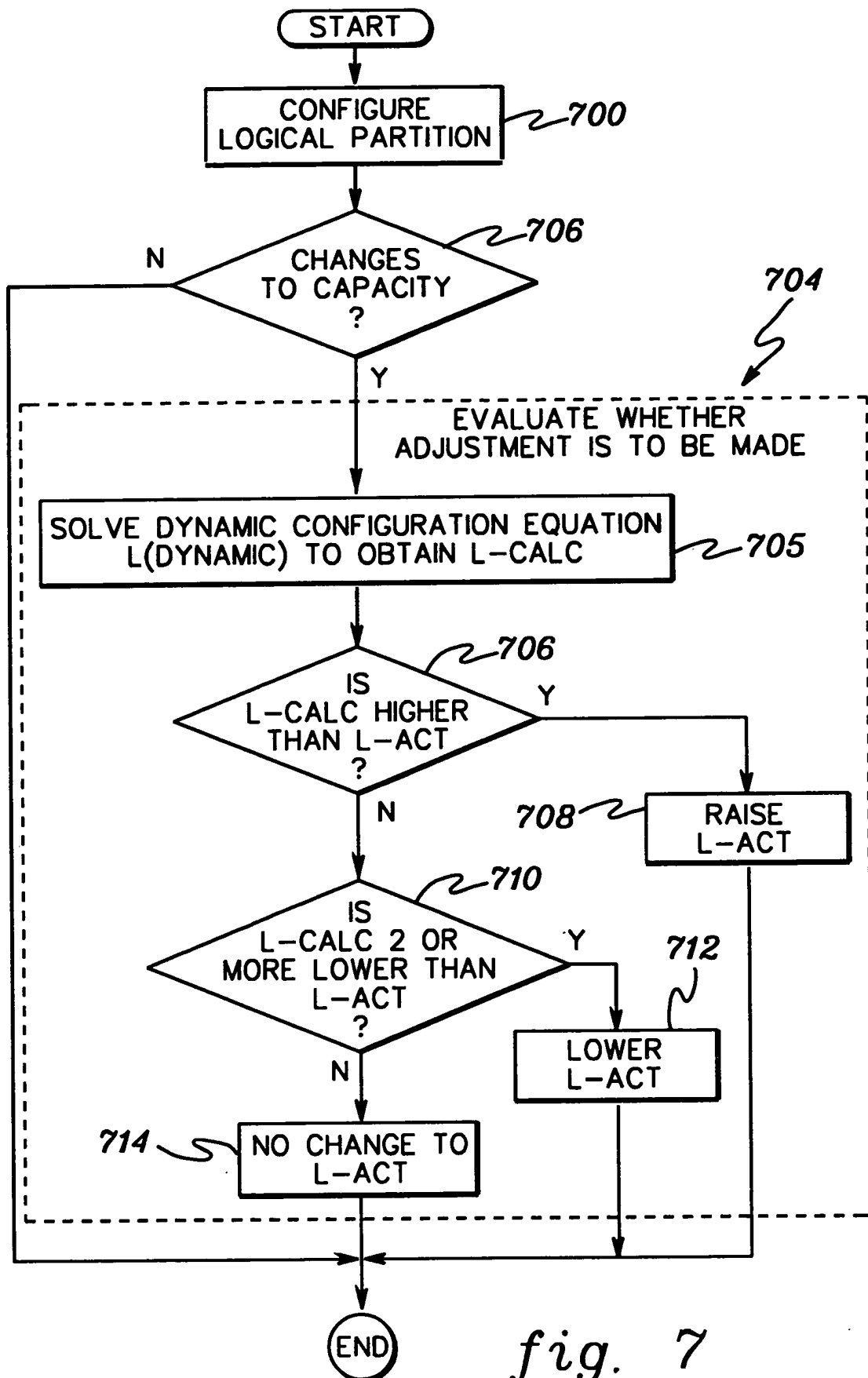


fig. 6

DYNAMIC ADJUSTMENT OF CONFIGURATION  
OF LOGICAL PROCESSORS*fig. 7*

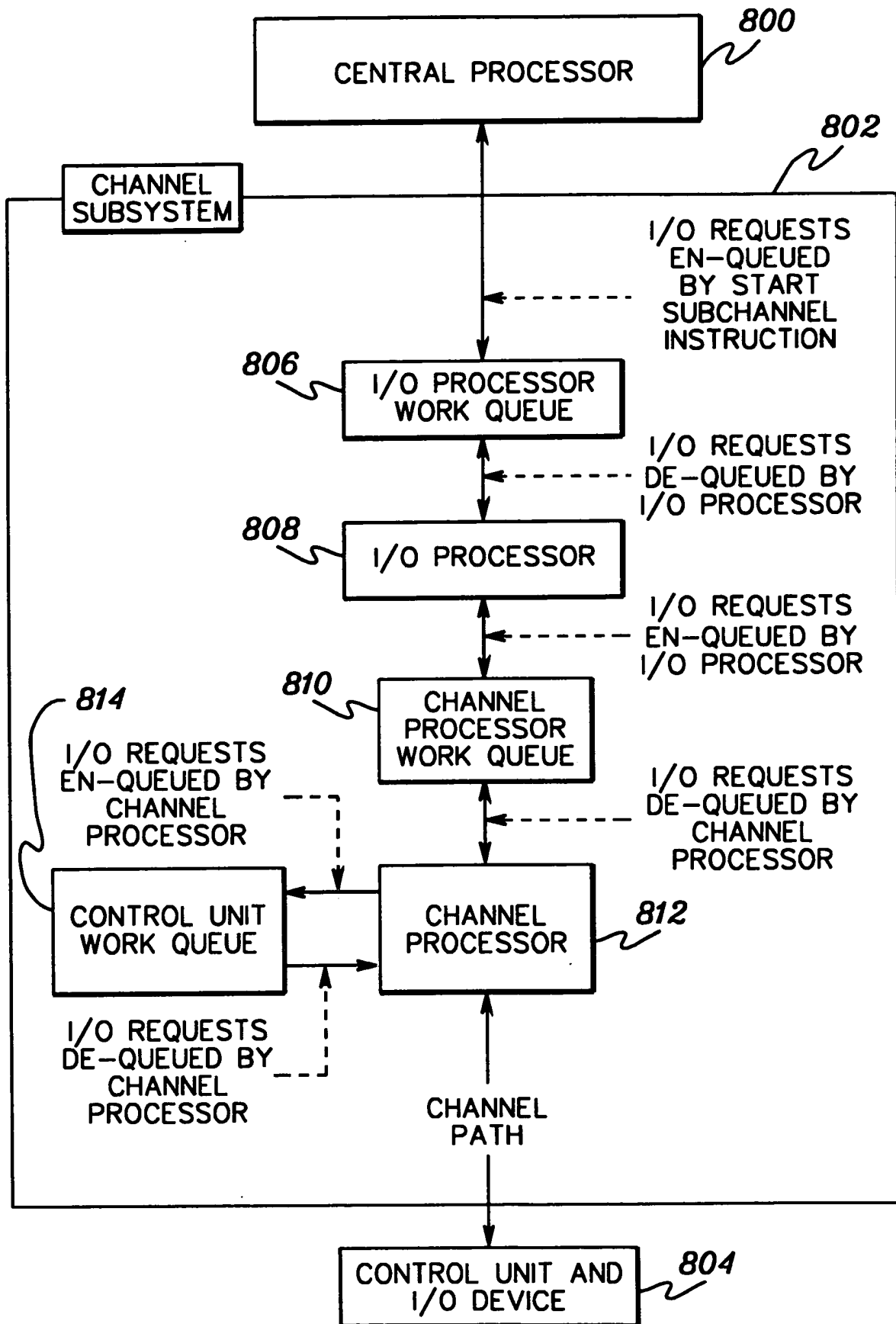


fig. 8



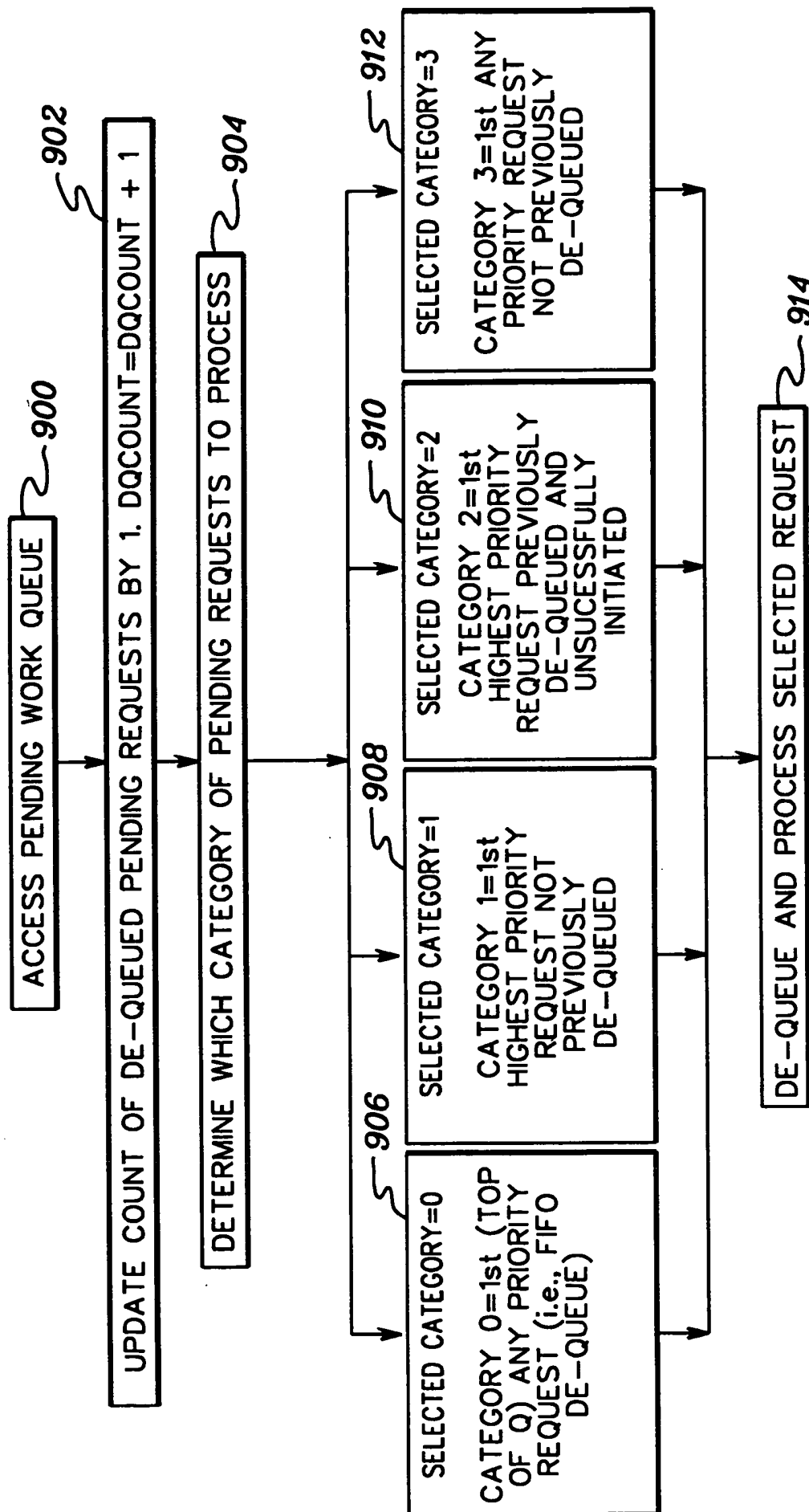
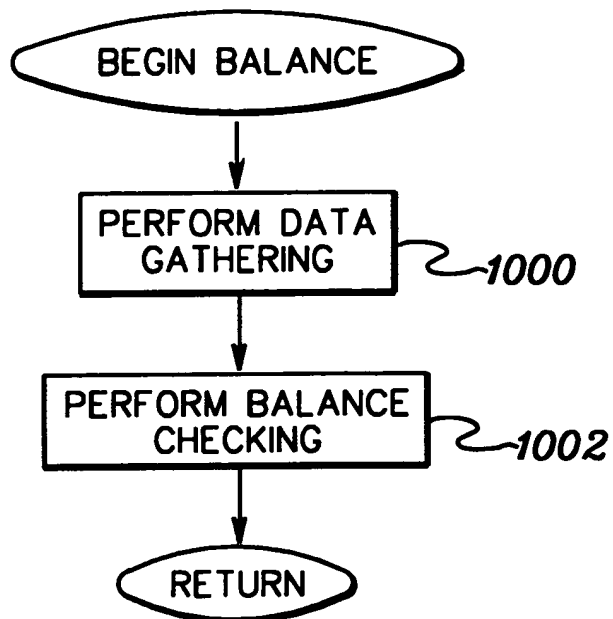
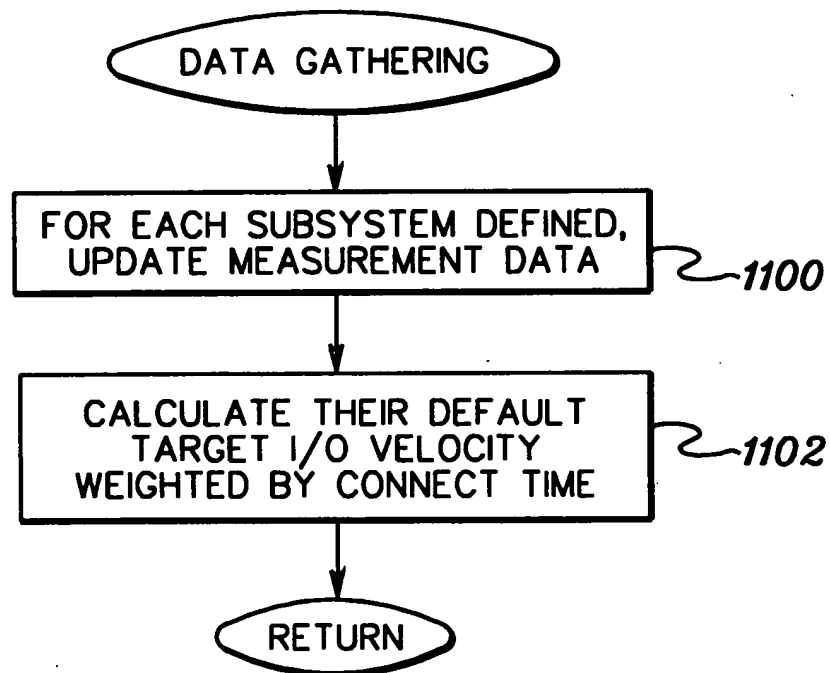


fig. 9

*fig. 10**fig. 11*

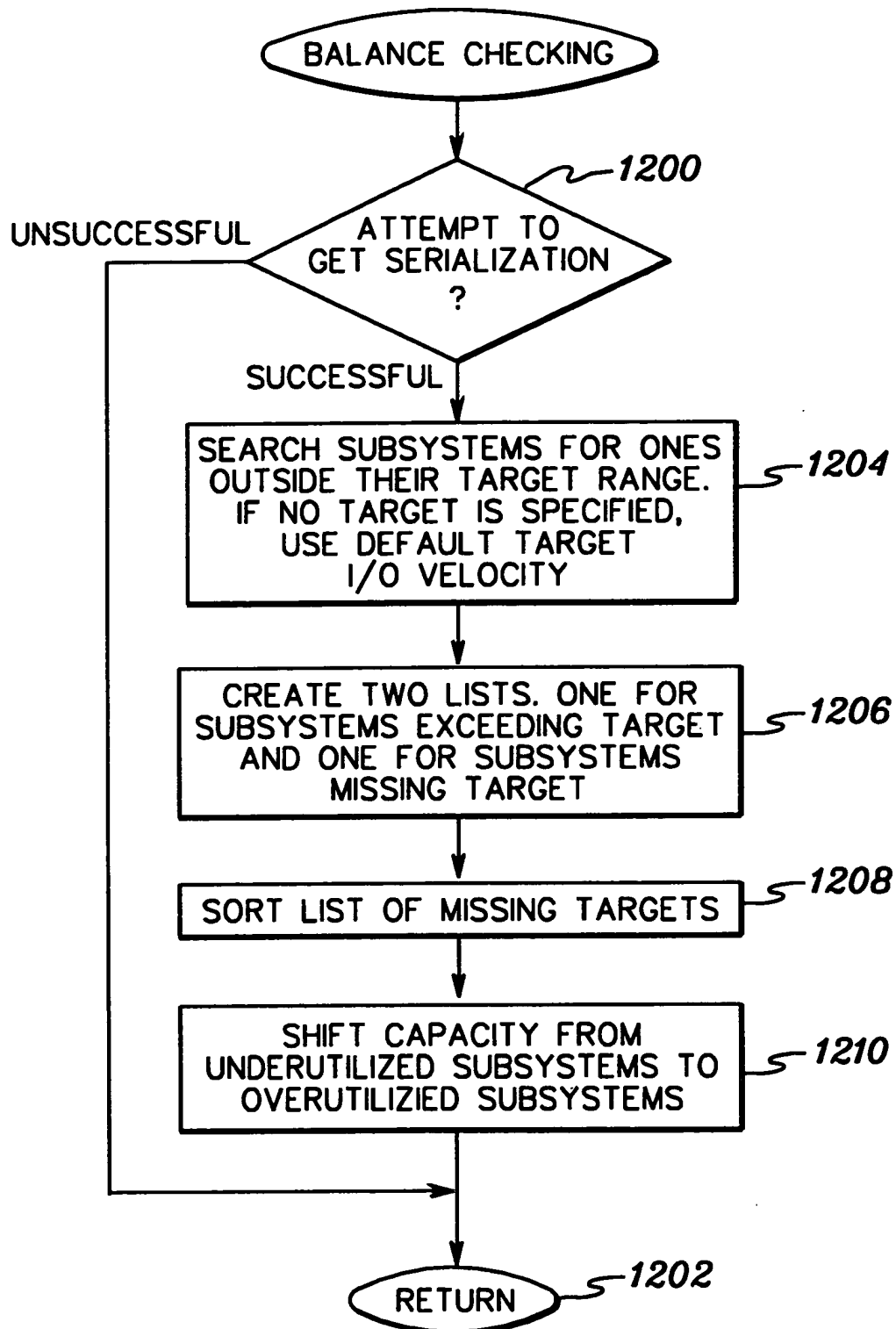
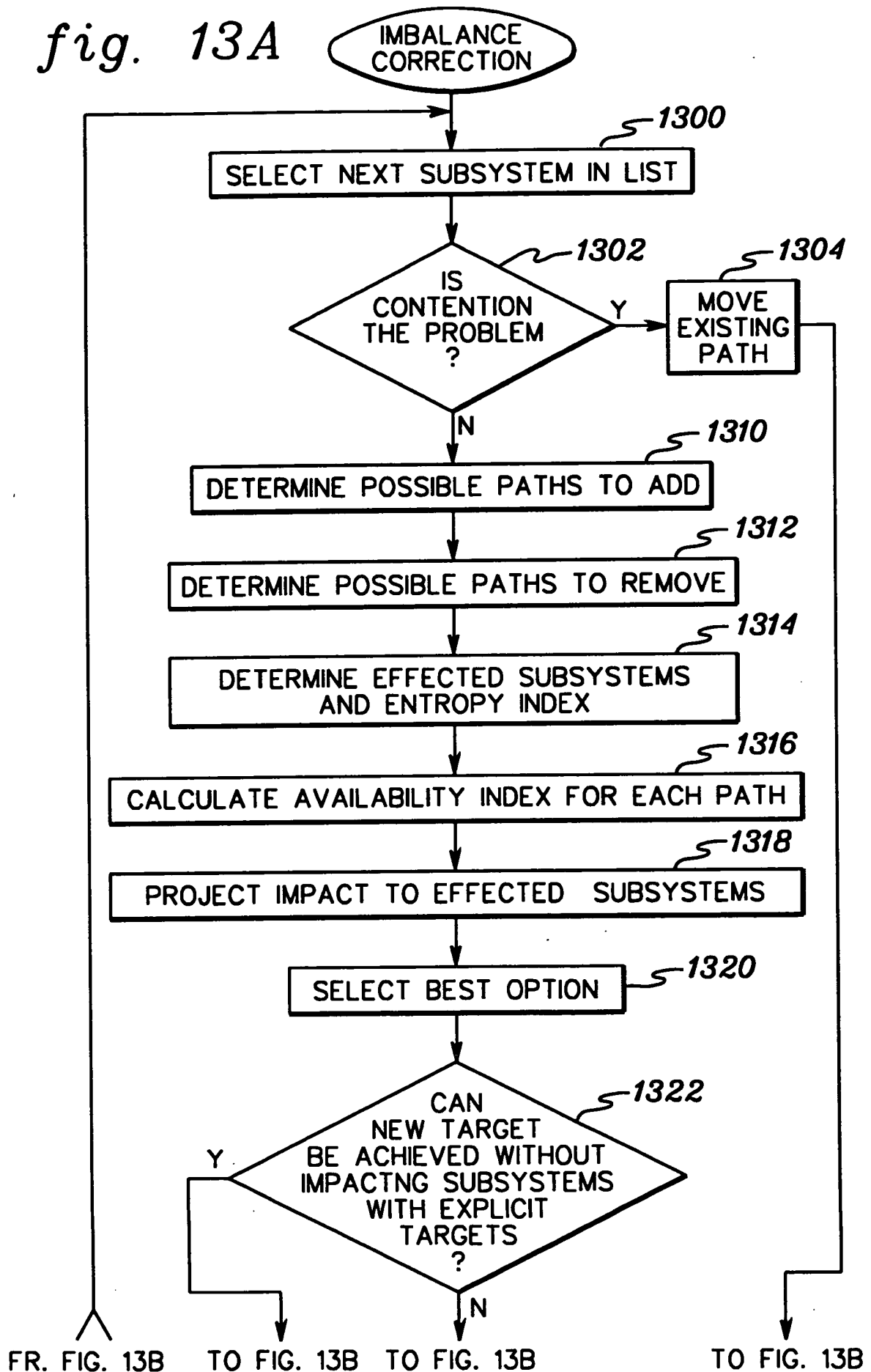
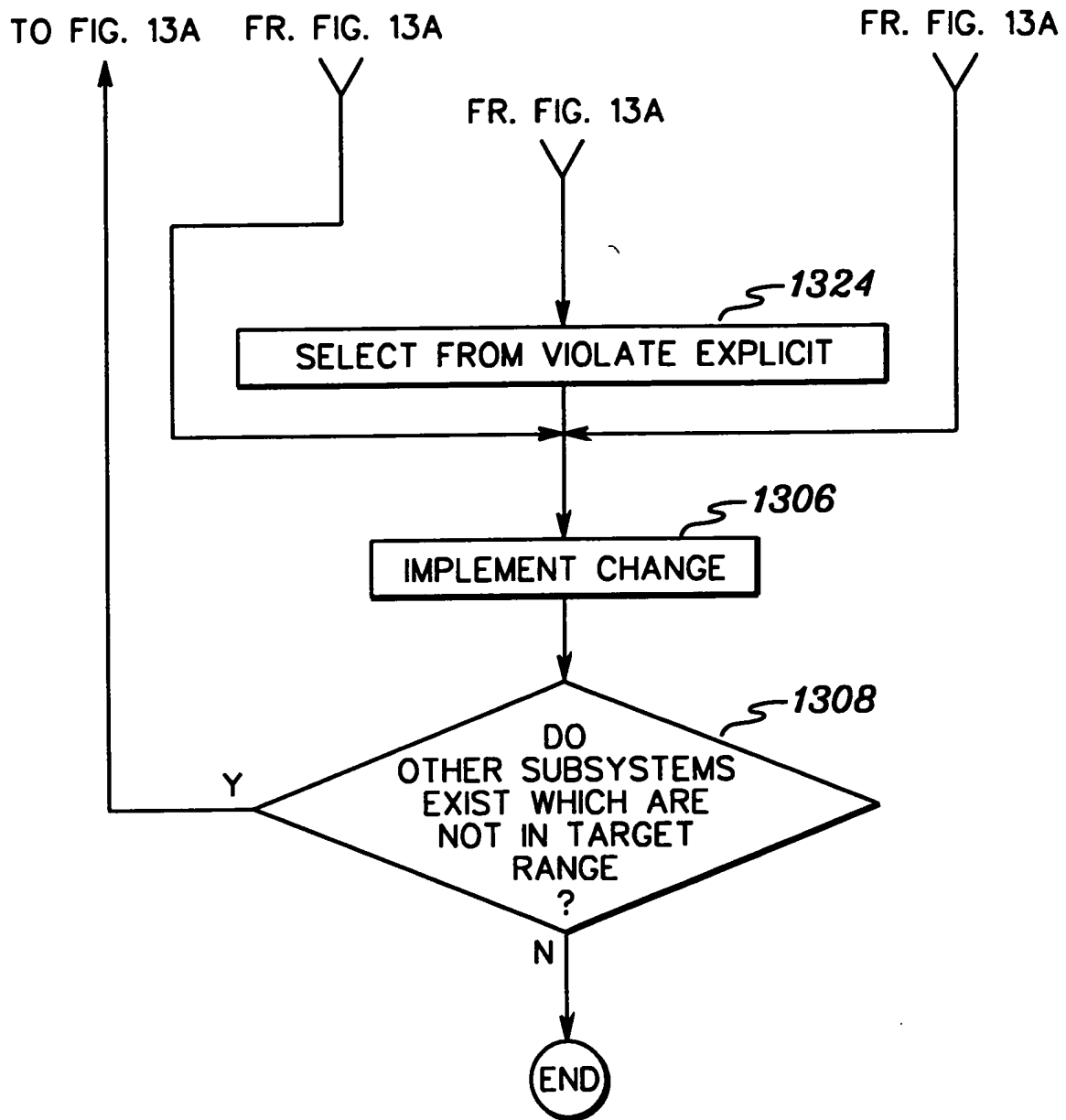
*fig. 12*

fig. 13A





*fig. 13B*

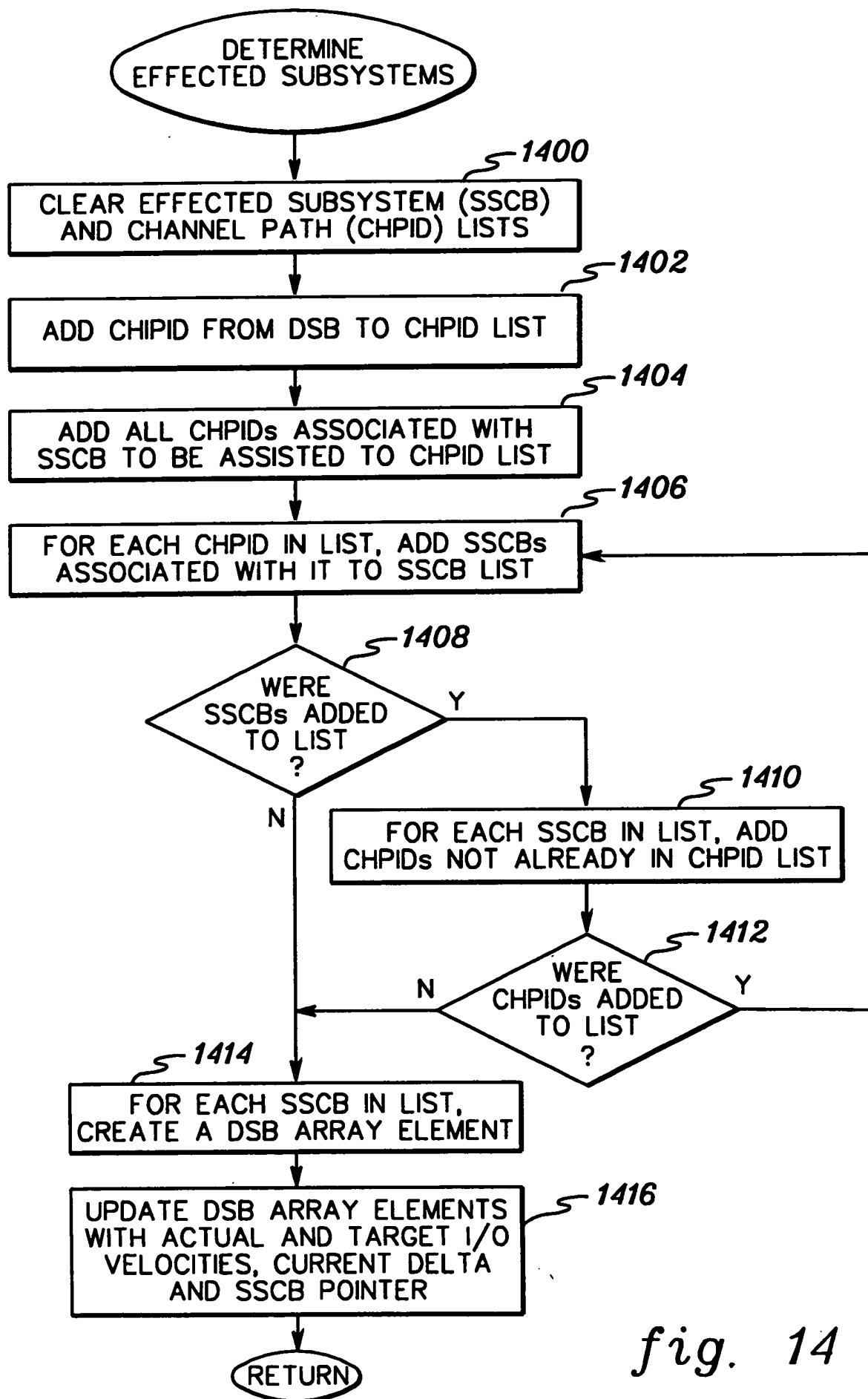
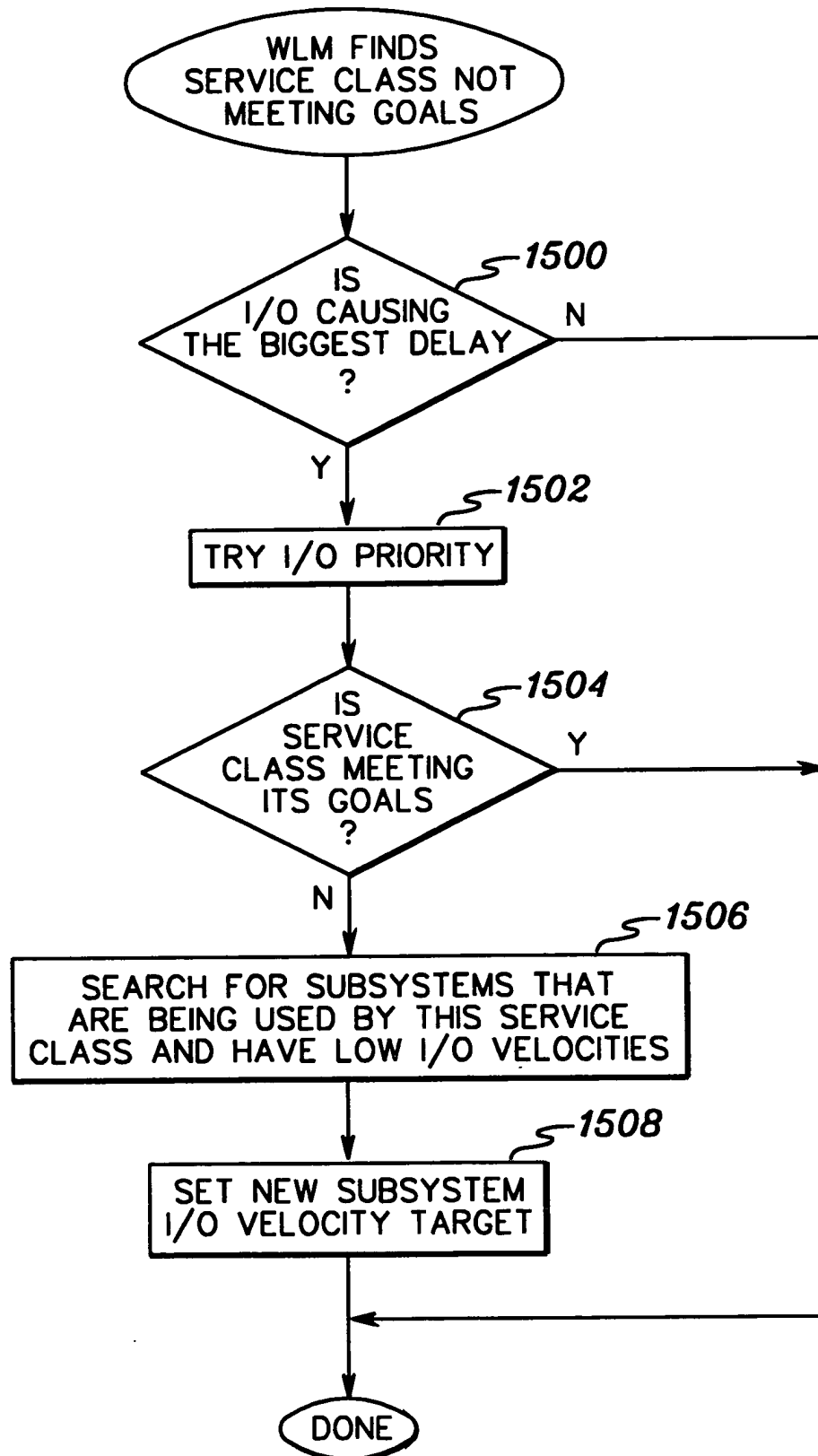
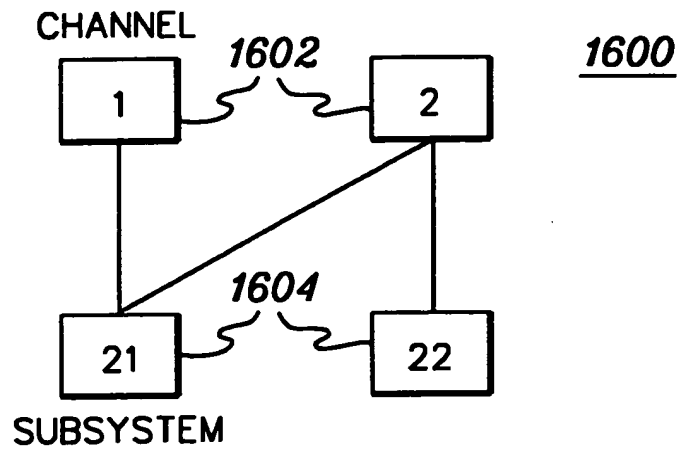
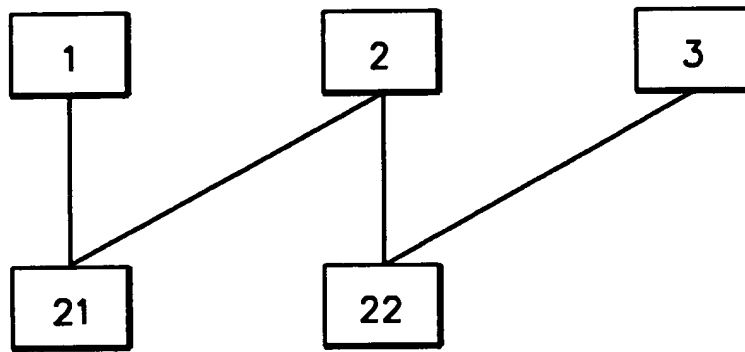


fig. 14

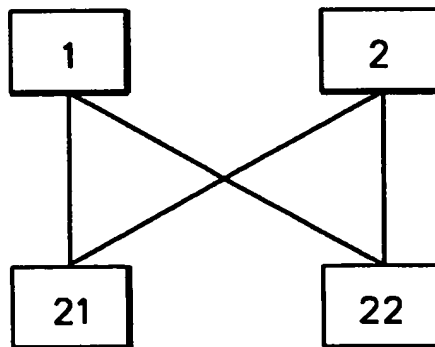
*fig. 15*



*fig. 16A*

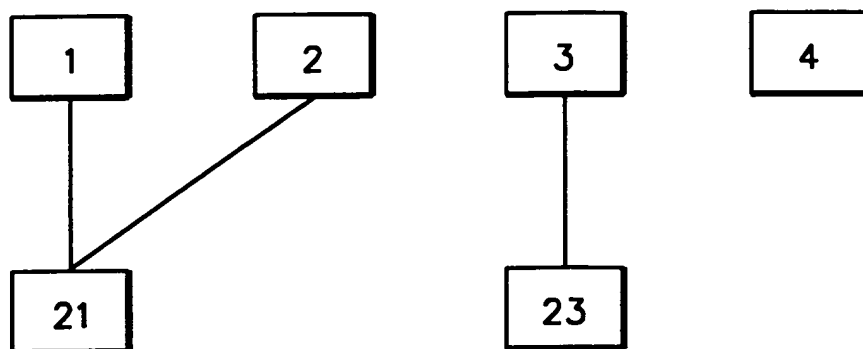


*fig. 16B*

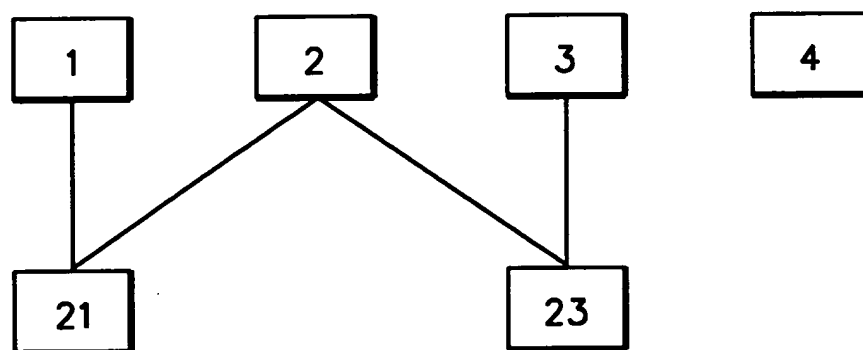


*fig. 16C*

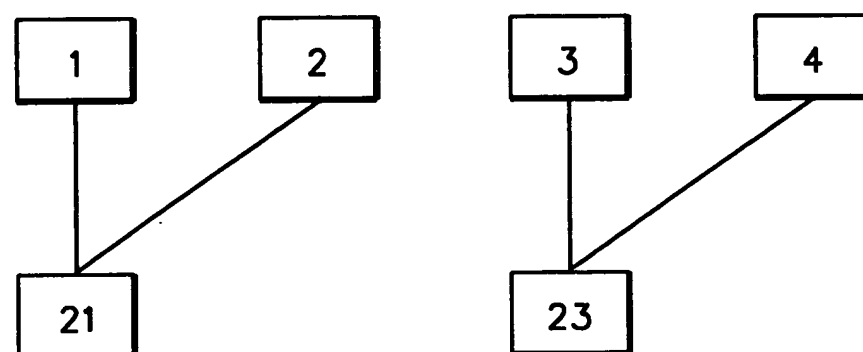




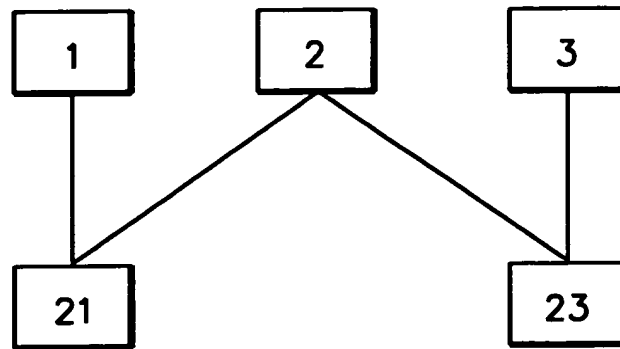
*fig. 17A*



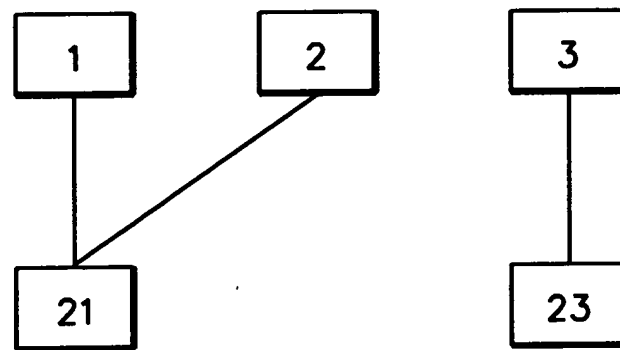
*fig. 17B*



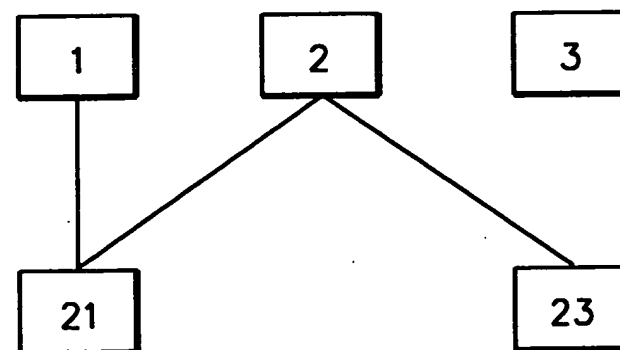
*fig. 17C*



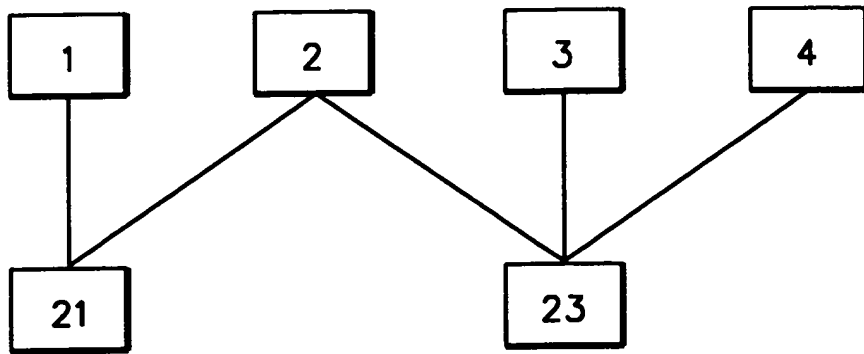
*fig. 18A*



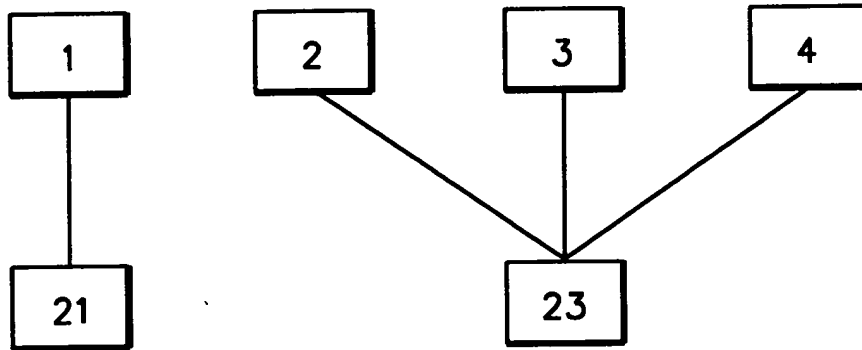
*fig. 18B*



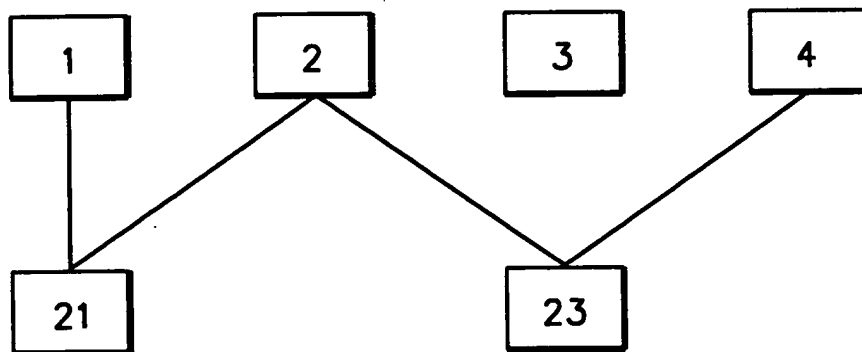
*fig. 18C*



*fig. 19A*



*fig. 19B*



*fig. 19C*

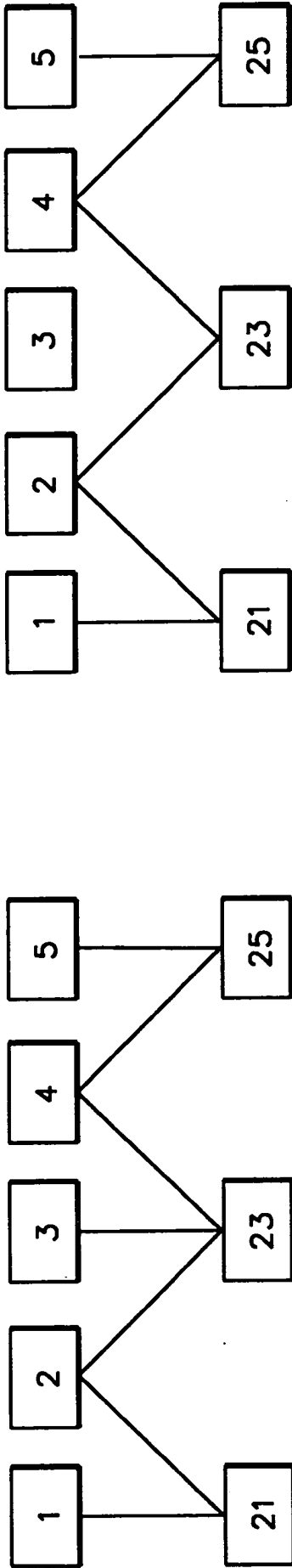


fig. 20B

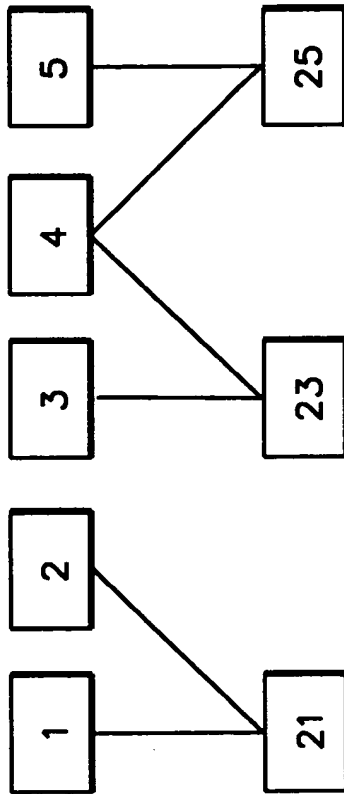
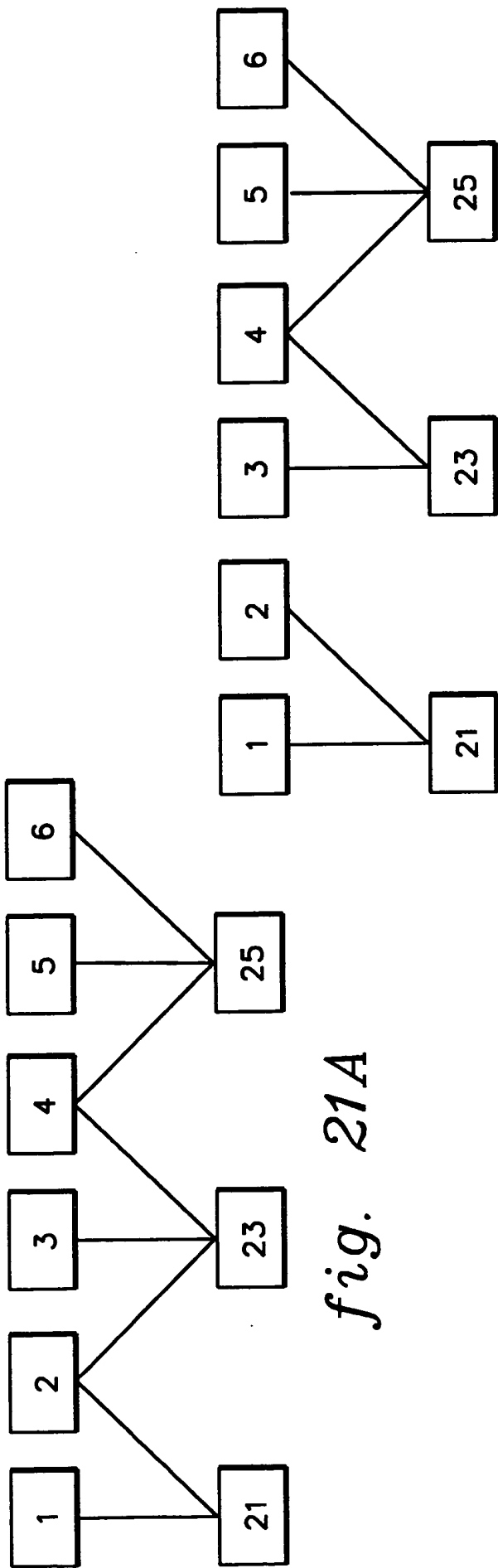
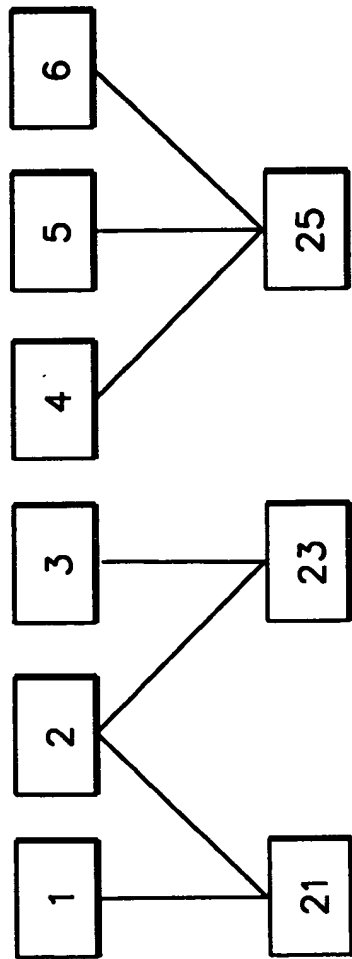


fig. 20C

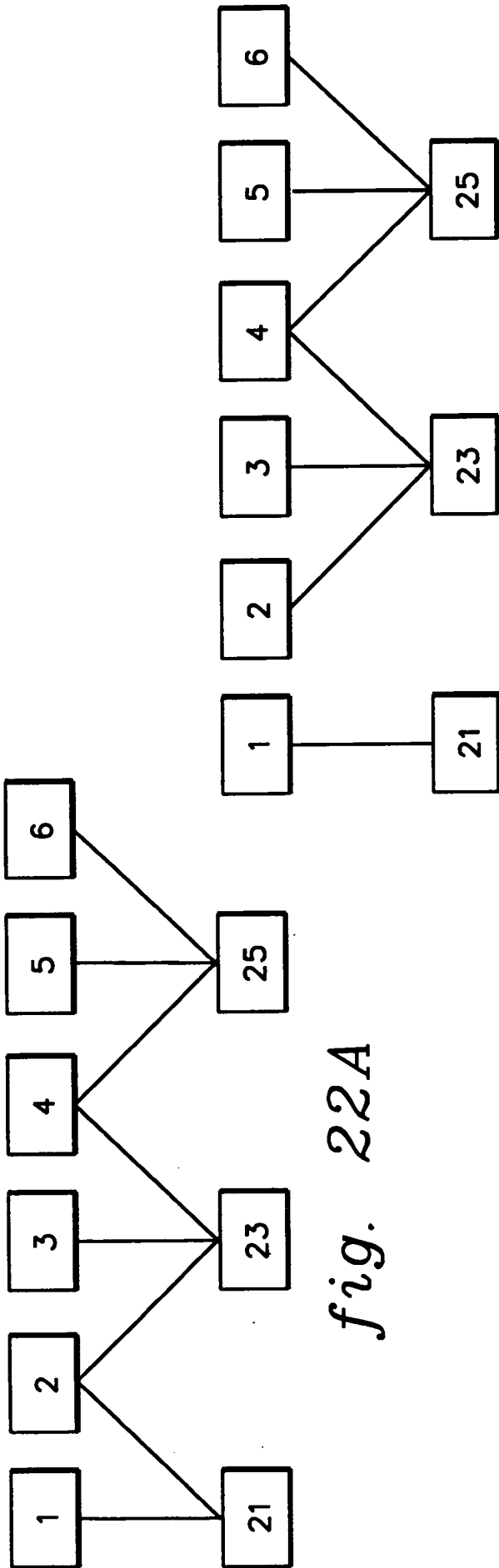


*fig. 21A*

*fig. 21B*

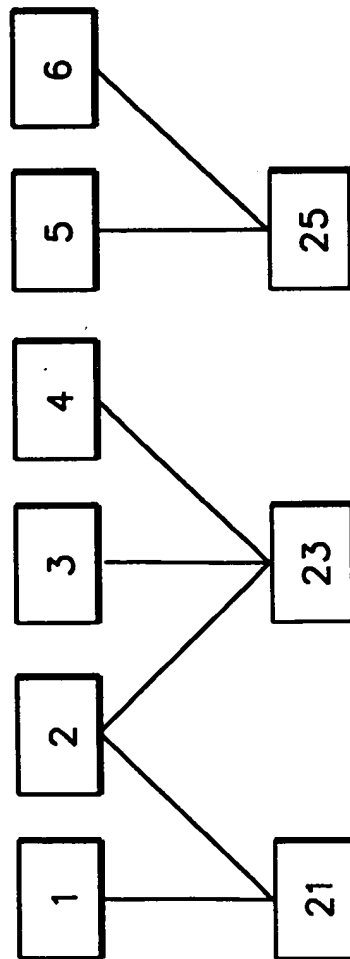


*fig. 21C*



*fig. 22A*

*fig. 22B*



*fig. 22C*